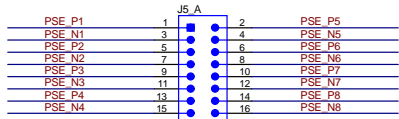
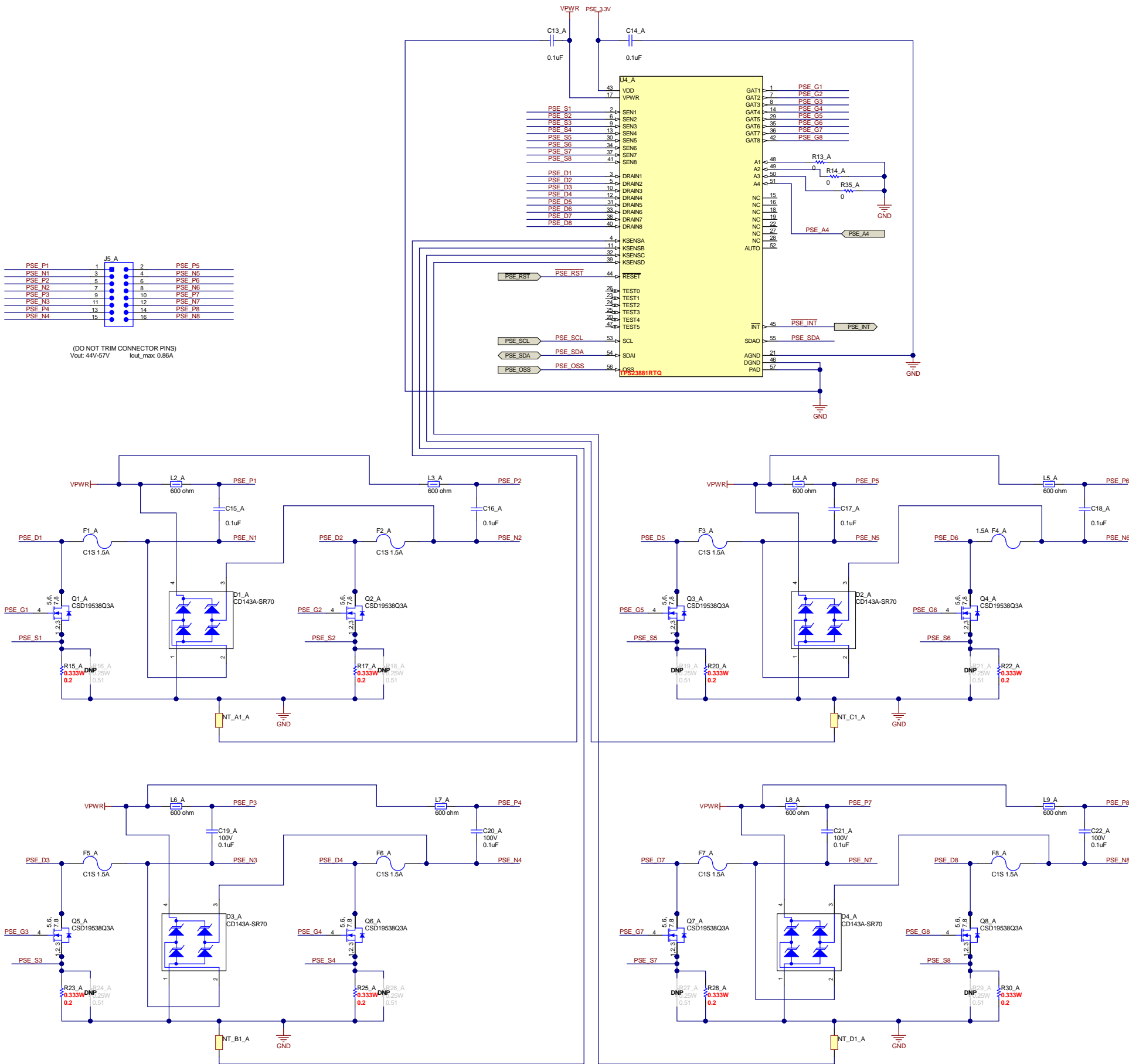


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Orderable: TPS23881EVM-083	Designed for: Public Release	Mod. Date: 4/12/2019
TID #: TIDA-050026	Project Title: TPS2388xEVM-083	
Number: PSIL083	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 1 of 3
Drawn By:	File: PSIL083A_SH1.SchDoc	Size: B
Engineer: P.Xu	Contact: http://www.ti.com/support	



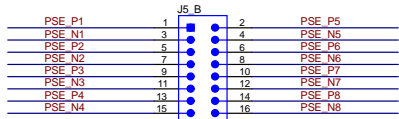
(DO NOT TRIM CONNECTOR PINS)
Vout: 44V-57V Iout_max: 0.86A



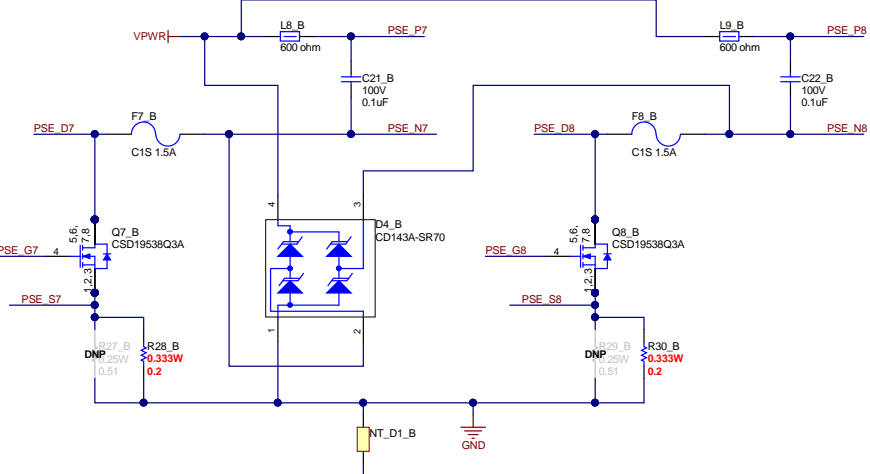
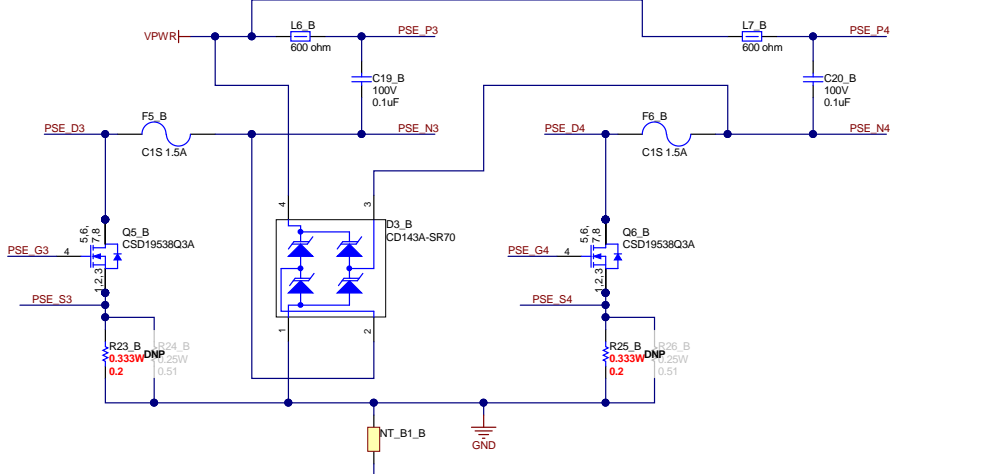
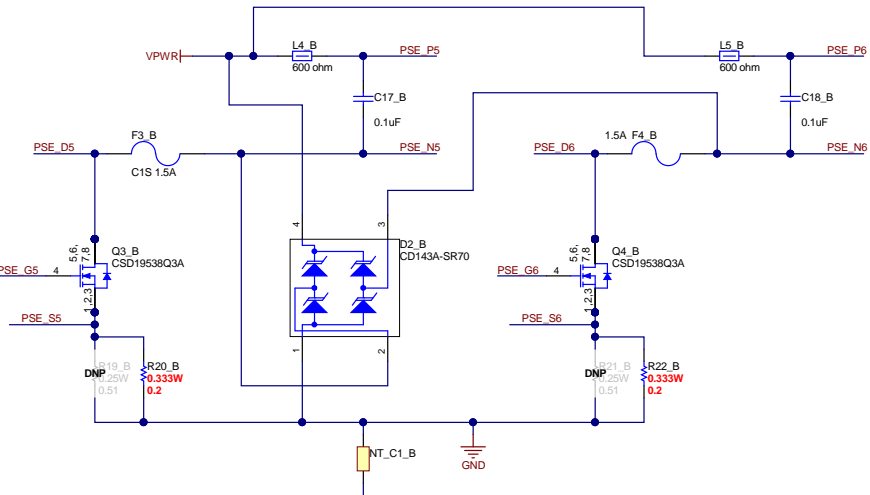
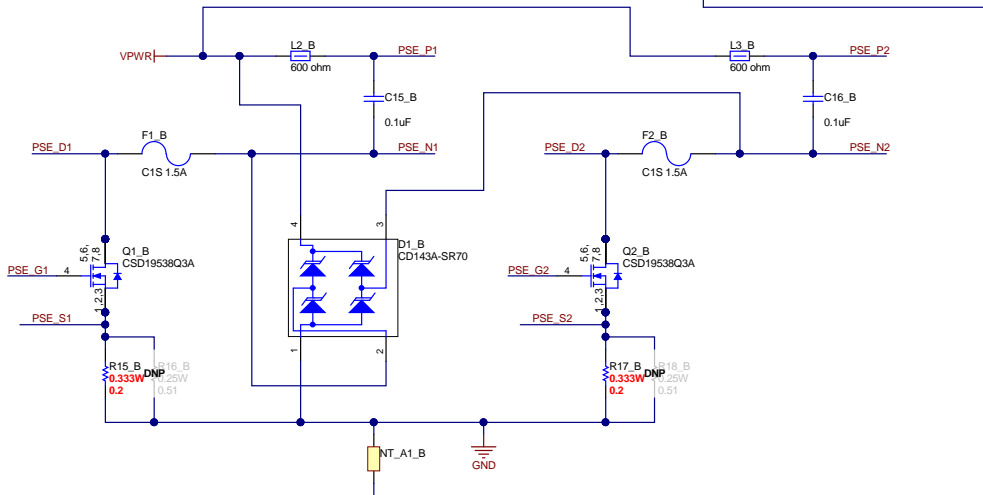
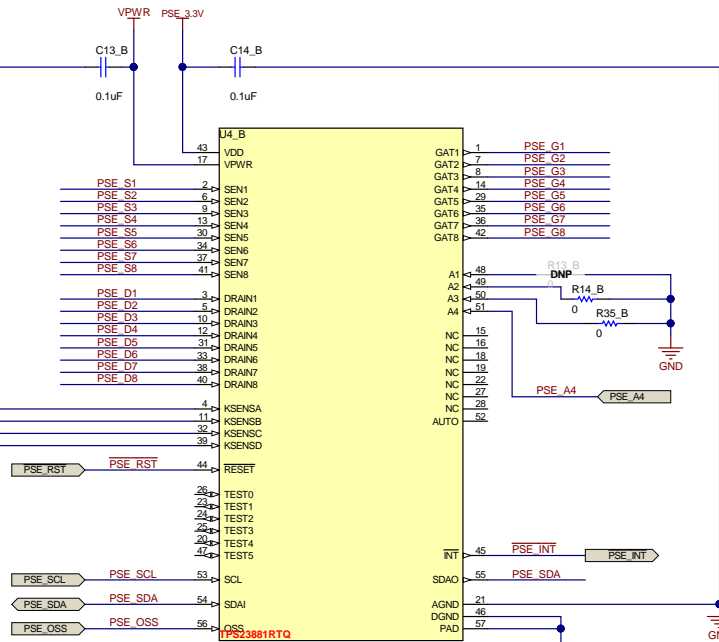
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Orderable: TPS23881VM-083	Designed for: Public Release	Mod. Date: 4/4/2019
TID #: TIDA-060026	Project Title: TPS23881VM-083	
Number: PSIL083	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 2 of 3
Drawn By:	File: PSIL083A_SH2_SchDoc	Size: C
Engineer: P.Xu	Contact: http://www.ti.com/support	





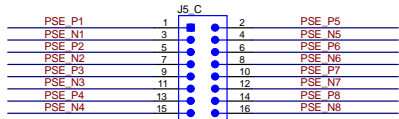
(DO NOT TRIM CONNECTOR PINS)
Vout: 44V-57V Iout_max: 0.86A



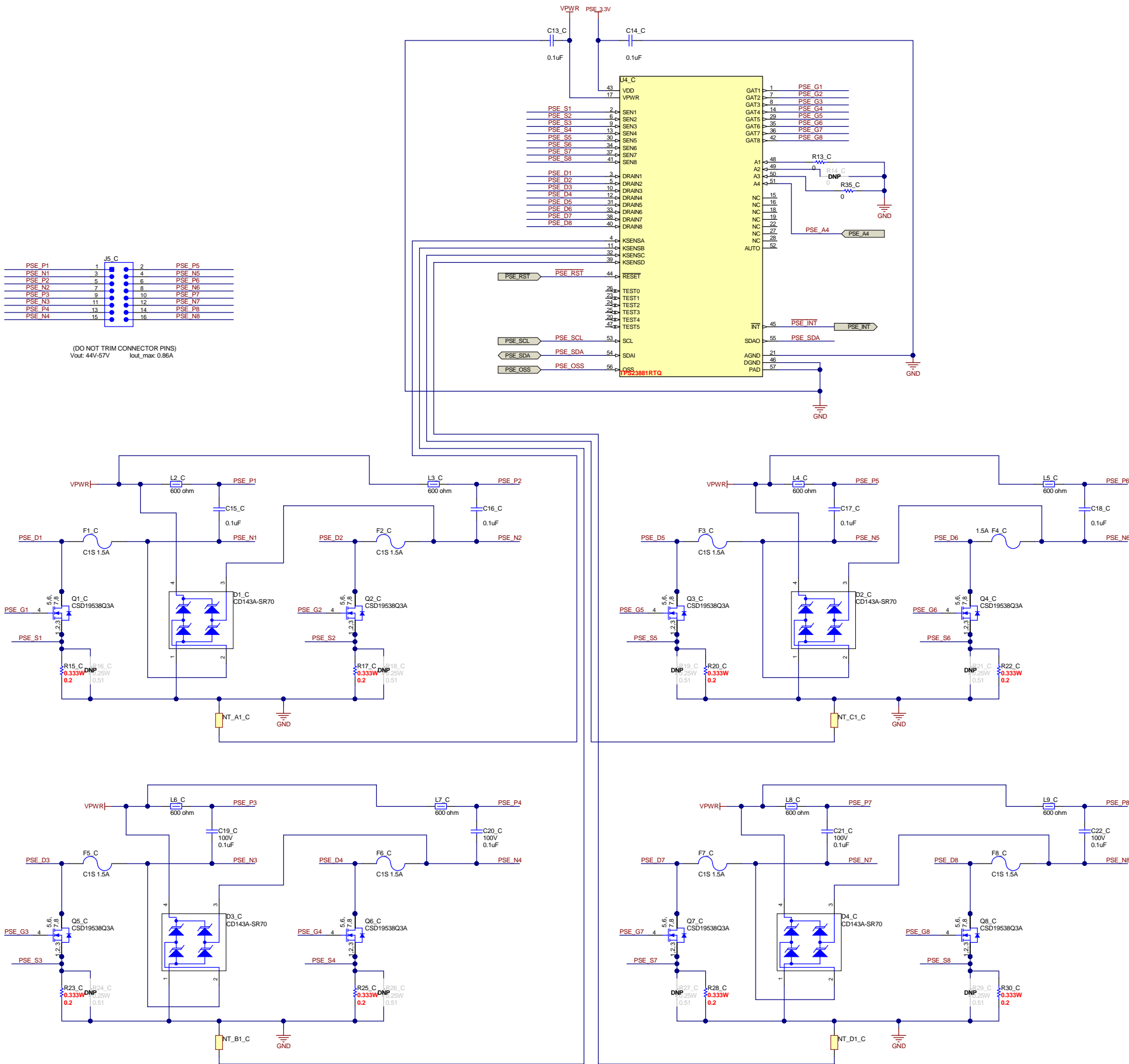
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Orderable: TPS23881VM-083	Designed for: Public Release	Mod. Date: 4/4/2019
TID #: TIDA-060026	Project Title: TPS23881VM-083	
Number: PSIL083	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 2 of 3
Drawn By:	File: PSIL083A_SH2_SchDoc	Size: C
Engineer: P.Xu	Contact: http://www.ti.com/support	http://www.ti.com





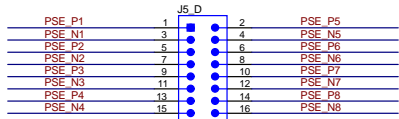
(DO NOT TRIM CONNECTOR PINS)
Vout: 44V-57V Iout_max: 0.86A



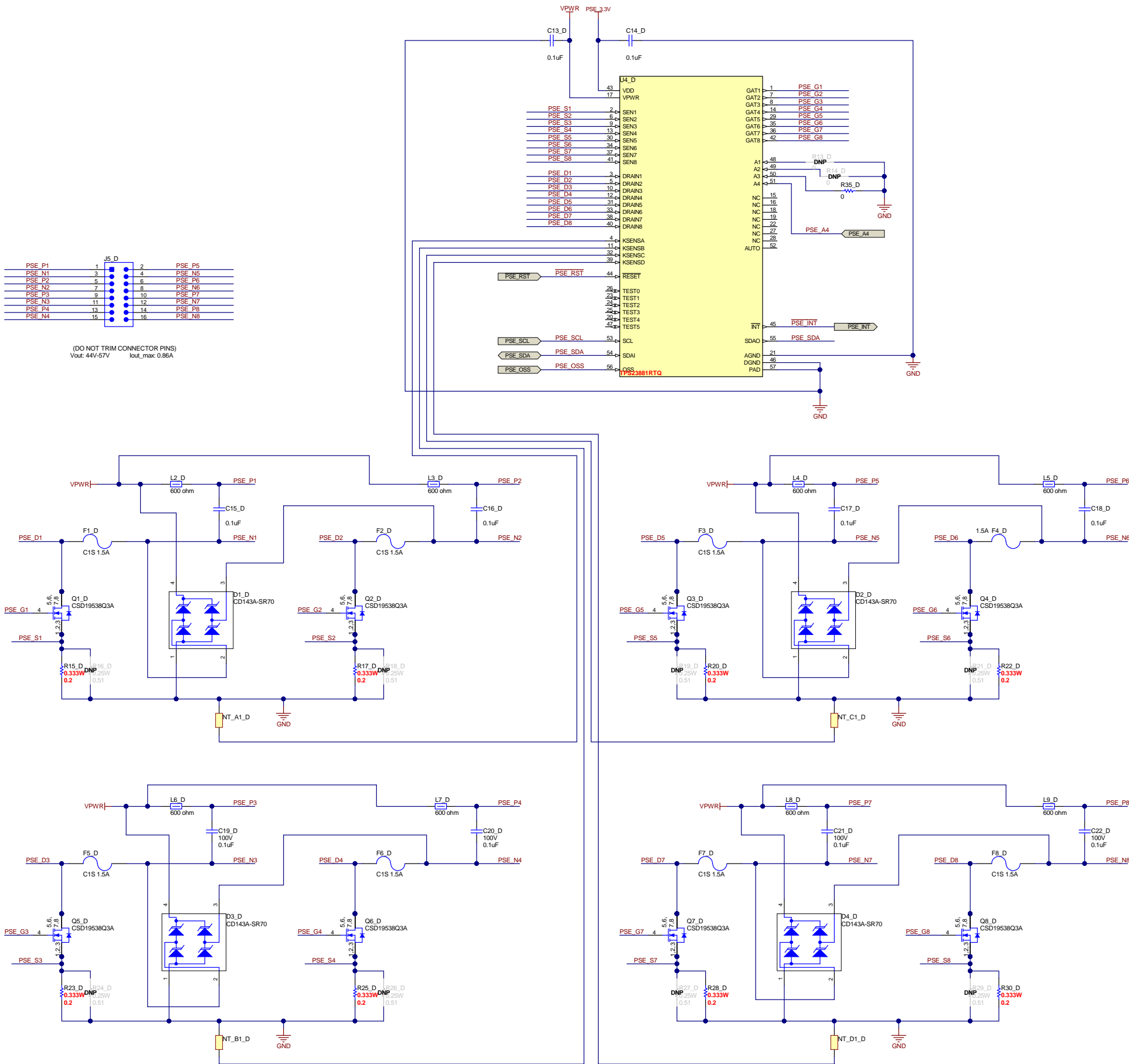
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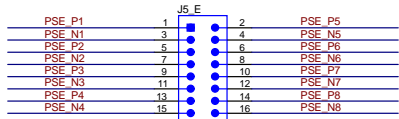
Orderable: TPS23881VM-083	Designed for: Public Release	Mod. Date: 4/4/2019
TID #: TIDA-060026	Project Title: TPS23881VM-083	
Number: PSIL083	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 2 of 3
Drawn By:	File: PSIL083A_SH2_SchDoc	Size: C
Engineer: P.Xu	Contact: http://www.ti.com/support	



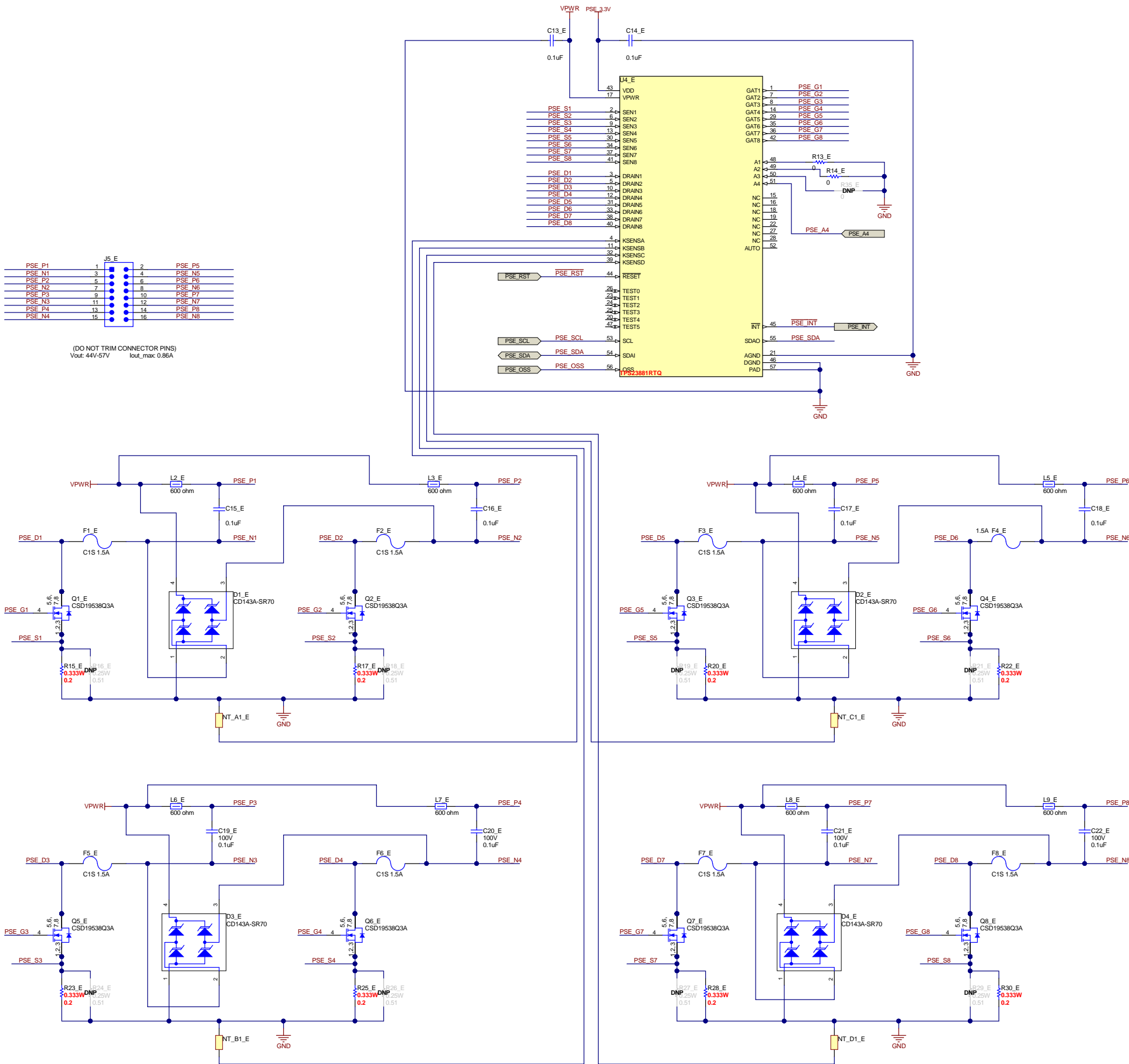


(DO NOT TRIM CONNECTOR PINS)
Vout: 44V-57V Iout_max: 0.86A





(DO NOT TRIM CONNECTOR PINS)
Vout: 44V-57V Iout_max: 0.86A

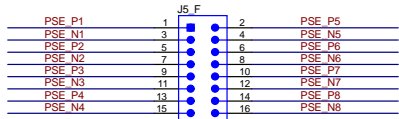


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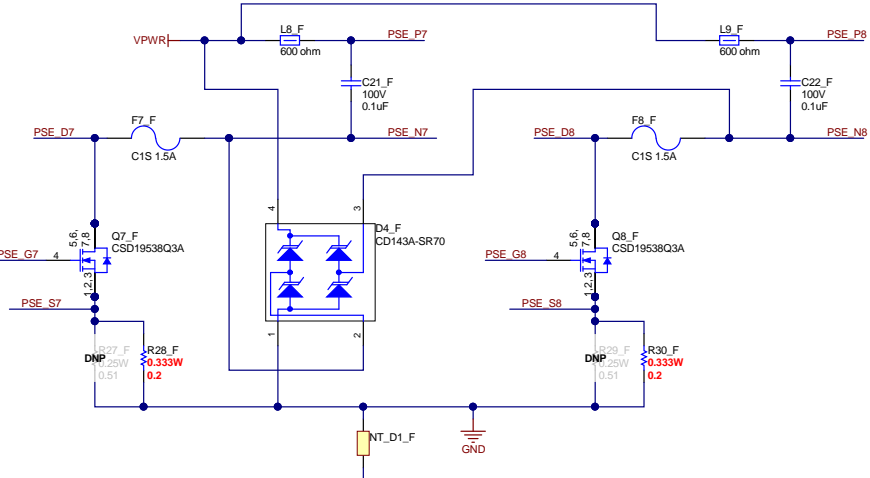
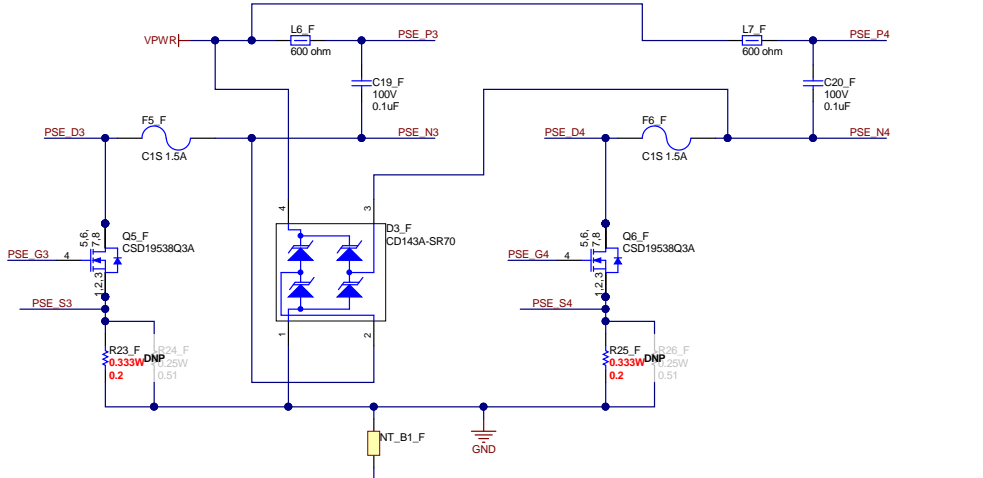
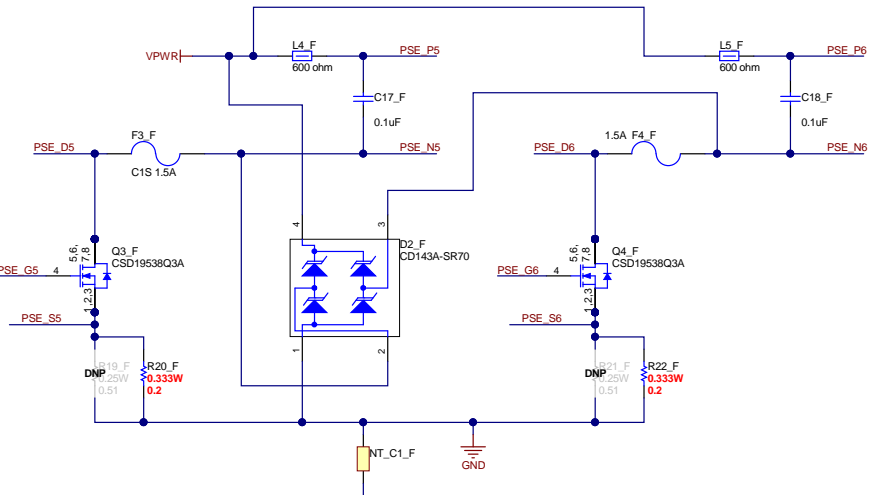
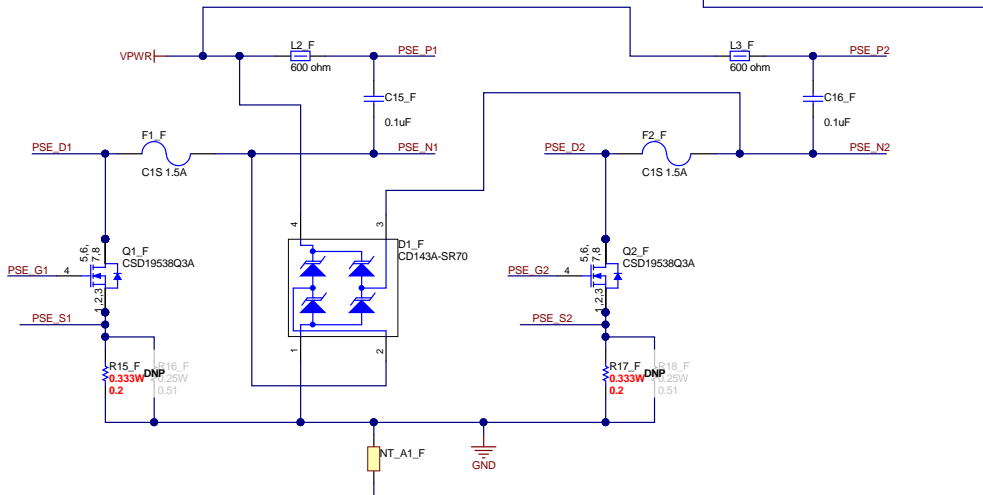
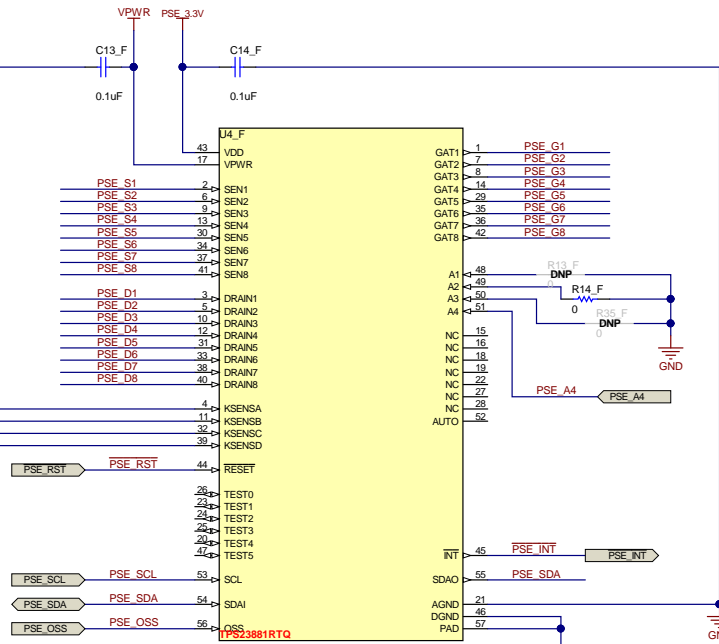
Orderable: TPS23881VM-083	Designed for: Public Release	Mod. Date: 4/4/2019
TID #: TIDA-060026	Project Title: TPS23881VM-083	
Number: PSIL083	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 2 of 3
Drawn By:	File: PSIL083A_SH2_SchDoc	Size: C
Engineer: P.Xu	Contact: http://www.ti.com/support	



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(DO NOT TRIM CONNECTOR PINS)
Vout: 44V-57V Iout_max: 0.86A



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Orderable: TPS23881VM-083	Designed for: Public Release	Mod. Date: 4/4/2019
TID #: TIDA-060026	Project Title: TPS23881VM-083	
Number: PSIL083	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 2 of 3
Drawn By:	File: PSIL083A_SH2_SchDoc	Size: C
Engineer: P.Xu	Contact: http://www.ti.com/support	



DNP

DNP

DNP

DNP

DNP

DNP

FID1

FID2

FID3

FID4

FID5

FID6

PCB Number: PSIL083
PCB Rev: A

PCB
LOGO
Texas Instruments


CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo



LBL1

PCB Label

THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	TPS23880EVM-083
002	TPS23881EVM-083

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5

Assembly Note

DO NOT TRIM CONNECTOR PINS on J5

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Orderable: TPS23881EVM-083		Designed for: Public Release	Mod. Date: 1/25/2019
TID #: TIDA-050026		Project Title: TPS2388xEVM-083	
Number: PSIL083	Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet: 3 of 3	
Drawn By:	File: PSIL083A_EVM_Hardware.SchDoc	Size: B	
Engineer: P.Xu	Contact: http://www.ti.com/support		